

### Abstract of the Disclosure

A data output driver of a combination type of a synchronous dynamic random access memory (SDRAM) device operated in both of a single data rate (SDR) mode and a double data rate (DDR) mode, the data output driver includes a first input/output line connected between a drain of a pull-up transistor and a data input/output pad, a second input/output line connected between a drain of a pull-down transistor and the data input/output pad, at least one switching unit formed on each of the first input/output line and the second input/output line, and at least one resistor parallel-connected with the switch and formed on each of the first input/output line and the second input/output line, wherein the switching unit is turned on or turned off by selecting one of a SDR mode and a DDR mode.